























Modern VLSI Design 4e: Chapter 1

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- Architecture: large blocks.
- Logic: gates + registers.
- Circuits: transistor sizes for speed, power.
- Layout: determines parasitics.

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Top-down vs. bottom-up design

- Top-down design adds functional detail. - Create lower levels of abstraction from upper levels.
- Bottom-up design creates abstractions from low-level behavior.
- Good design needs both top-down and bottom-up efforts.

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	System Specification	•]			
Behavioral Representatio	Functional Verification		••		
Logic (Gate-level) Re¤resentati	Logic Design Logic Verification	 			
	Circuit Design	Device M	odeling		
Circuit Representatio	Circuit Verification	ļ	Simulation	Example	Signal value
Lavout	Physical Design		SWITCH GATE FUNCTION	TRANSISTOR AND gata ADDER etc.	0/1, strength 0/1 0/1
Representation	Fabrication & Testing	Copyright © 2008 Prentice Hall			

Design validation

- Must check at every step that errors haven't been introduced-the longer an error remains, the more expensive it becomes to remove it.
- Forward checking: compare results of lessand more-abstract stages.
- Back annotation: copy performance numbers to earlier stages.

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Manufacturing test

- Not the same as design validation: just because the design is right doesn't mean that every chip coming off the line will be right.
- Must quickly check whether manufacturing defects destroy function of chip.
- Must also speed-grade.

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